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## **Dual Row Qfn Package Assembly**

Dual Row Quad Flat No lead (DR-QFN) is an ideal solution for such demanding applications; however, despite the simplicity of its package structure, it possesses challenges in various assembly processes. This paper describes

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the problems that are associated with the different concepts of both 'saw' and 'punch' singulated DR-QFN and the

## **Design and Process Optimization for Dual Row QFN**

Leadless Quad Flat Pack (QFN) packages are plastic-encapsulated with a copper lead frame substrate, providing a robust, low-cost

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solution for small form factor applications such as mobile handsets and other battery operated consumer products.

Dual-row QFN packages have interstitial, staggered contacts. The inner row is offset 0.5 mm,

## **AN016: Dual-Row QFN Package Assembly and PCB Layout Guidelines**

The dual-row or multi-

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row QFN package utilizes an interstitial lead design that results in a staggered lead arrangement. The inner row is offset 0.5 mm, which results in a compact design that maximizes die size while not exceeding the surface mount technology (SMT) capability of a typical 0.5 mm pitch SMT process.

**Assembly and PCB**

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## Package

### **Layout Guidelines for QFN Packages**

Product Overview The problem of limited pin counts associated with QFN packages for highly integrated wireless system is addressed by using dual-row QFN packages instead of the traditional QFN packages in which two rows of surface mount pads are placed at the periphery of the package. Both rows



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can then be connected to the die using wire bonds.

## **Dual Row QFN - ASE KOREA**

Quad Flat No-Lead (QFN) and Dual Flat No-Lead (DFN) package offering includes QFNs, QFNp, VQFN, WQFN, UQFN, XQFN, QFNp-dr and QFNs-mr. We offer multiple QFN configurations in either punch or saw singulated formats.

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Punch singulated packages are individually “punched” from molded strips during final assembly; saw singulated

### **Quad Flat No-Lead Package - STATS ChipPAC Ltd**

The QFN/SON package are assembled using two different methods:

- The sawn-type is molded in a single mold cavity or mold array process (MAP)

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and separated into individual packages during a final saw process. • The punch-type is molded in individual cavities and separated using a punch tool. NXP Semiconductors AN1902

## **Assembly guidelines for QFN (quad flat no-lead) and SON ...**

INTRODUCTION This package application note provides the

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guidelines for the handling and assembly of Microchip Dual Row Quad Flat No-Leads (DQFN) packages during Printed Circuit Board (PCB) assembly. In addition, it provides general information for the PCB land pattern design and component rework guidelines.

## **Package Application Note for DQFN Packages**

MLF and DRMLF are

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also known as quad-flat no-leads (QFN) and dual-quad-flat no-leads (DQFN) respectively, in semiconductor packaging and SMT industry. Figure 1. Dual Row MLF® Package Photo and Cross Section Drawings Surface Mount Considerations for DRMLF Package Several primary factors that can affect the mounting of the package on the board:

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## **Surface Mount Guidelines for Amkor Dual Row MicroLeadFrame**

Model simulated a JEDEC test board (JEDEC51-5)utilizing a dual sided metal 10 x 10 thermal via array at 3watt power & 20°C ambient. PACKAGE REPAIR GUIDELINES MRQFN Repair Procedure A package repair/rework station is strongly recommended

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for this process. (i.e. Air-VacEngineering, Metcal, or Den-OnInst.)  
Package Replacement Procedure:

## **Design Summary Multi-rowQuad Flat No-lead(MRQFN)**

Flat no-leads packages such as quad-flat no-leads (QFN) and dual-flat no-leads (DFN) physically and electrically connect integrated circuits to printed circuit boards.

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Flat no-leads, also known as micro leadframe (MLF) and SON (small-outline no leads), is a surface-mount technology , one of several package technologies that connect ICs to the surfaces of PCBs without through-holes .

## **Flat no-leads package - Wikipedia**

QFNp-dr is a punch singulated dual row package that features



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a significantly higher number of I/O terminal pads in a smaller footprint. The key to the increased performance capability of the QFNp-dr is in its leadframe design which features two rows of staggered I/O terminal pads with an exposed die pad for die grounding and improved thermal performance.

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## Package **Dual Row Qfn Package Assembly And Pcb Layout Guidelines ...**

QFN/SONs are LLP stands for "leadless lead frame package" and was the terminology for National's QFN/SON technology. TI has integrated LLP into the company's QFN/SON packages. Please see TI's package selection tool for more information. Does TI

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offer dual row or multi-row QFN/SON? Yes, TI offers dual row QFN.

## **QFN/SON FAQs | Quality & reliability FAQs | TI.com**

DRMLF Dual-row MLF  
[13] aQFN Advanced  
QFN [14] Array QFN  
Array QFN [15] DFN  
Dual flat no-lead  
package [16] NBA-QFN  
No bump array QFN  
[17] TQFN Thin QFN  
[18] VQFN/WQFN Very  
thin QFN [19] LGA Land

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grid array [20] 2.3 QFN Packages and Assembly In a 2003 paper [21], A. Syed and K. WonJoon stated that within the last few years, the QFN package has

## **Quad Flat No-Lead (QFN) Evaluation Test**

The “Dual Flat No-lead” DFN is a cousin of the QFN having SMT leadless interconnects only on two sides of

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the package. There are two common QFN designs referred to as molded and open cavity. The most common type is the plastic molded package. The second type is an open cavity package and comes with a lid to seal the package.

## **QFN DESIGN CONSIDERATIONS TO IMPROVE CLEANING**

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Integrated circuits are put into protective packages to allow easy handling and assembly onto printed circuit boards and to protect the devices from damage. A very large number of different types of package exist. Some package types have standardized dimensions and tolerances, and are registered with trade industry associations such as JEDEC and Pro

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## Assembly And Pcb Layout Lines **List of integrated circuit packaging types - Wikipedia**

Newer technologies have taken the dual-row QFN a step further. The etched leadless package (ELP) offers flexibility with up to 3 rows with isolated pads, power and ground rings. This gives the device manufacturer options to optimize product



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performance. The triple-row capability provides an even smaller footprint having more I/Os.

## **WLCSP QFN ELP Semiconductor packaging for consumer ...**

Not any more. ZigBee radios pretty much only come in QFN packages. Some of the newest embedded processors only come in .4mm BGA

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packages. I could go on. You just can't hand assembly these bad boys anymore. That may seem to make things more difficult, but not really. If you simply can't do it, you have to send it out and you can get back to ...

## **PCB Assembly Services - Screaming Circuits: Need help with ...**

While DR-QFN  
packages share

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common

characteristics with conventional QFNs in terms of design, performance and reliability<sup>1 2 3</sup>, surface mounting stands out as a major challenge in the package ...

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