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WebPACK from Xilinx,  
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Implementation Using  
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**(PDF) Introduction**

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constructs and the  
finite state machine for  
digital signal  
processing,  
communications and  
control with the FPGA,  
external hard core and  
internal soft core  
peripherals.

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maintain is support.  
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research I believe the  
books source code is  
here. Here is some  
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about software and  
hardware as well as a  
email address to  
contact the for any  
further questions which  
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Digital Logic Design.  
Syllabus; For VHDL  
material, see Tutorial:  
VHDL for FPGAs; Useful  
links: VHDL examples  
for Nexys-3 Board:  
VHDL Workshop; Prof.  
Hanna's book:  
Introduction to Digital  
Design Using Digilent  
FPGA Boards - Block  
Diagram/VHDL  
Examples

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To begin, connect the PmodAD5 to JA connector of Nexys™ 3 board, pins JA1 to JA6 (see image below). You can use an extension cable for ease of use. Connect the USB cables from the PC to the board, one for programming (Digilent USB device) and one for the UART terminal (FT232R USB UART).

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