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and benefits. Before the RISC philosophy
became prominent, many computer

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(RISC) and Complex instruction set
computing (CISC). CISC has the capacity
to perform multi-step operations or
addressing modes within one instruction

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set. It is the CPU design where one instruction works several low-level acts. For instance, memory storage, loading from memory, and an arithmetic operation.

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Justin M. LaPre — CSCI 2500
Computer Organization

David Andrew Patterson (born November 16, 1947) is an American computer pioneer and academic who has held the position of Professor of Computer Science at the University of California, Berkeley since 1976. He announced

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